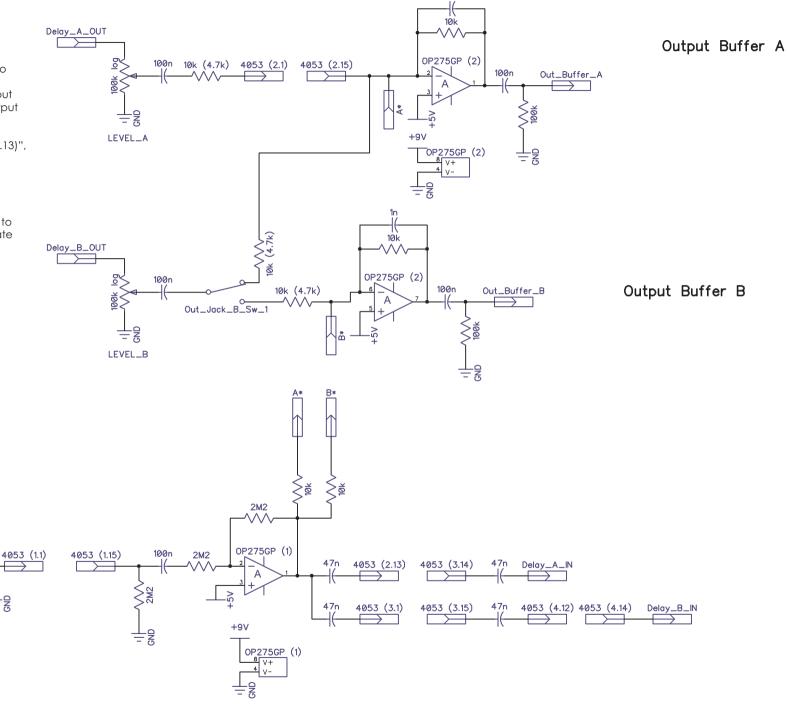


I have dual output buffers for use in stereo mode. Using a DPDT switched jack for Output B, Delay B can be routed to Output Buffer B, or mixed to Output Buffer A. Output Jack B is also switched in or out.

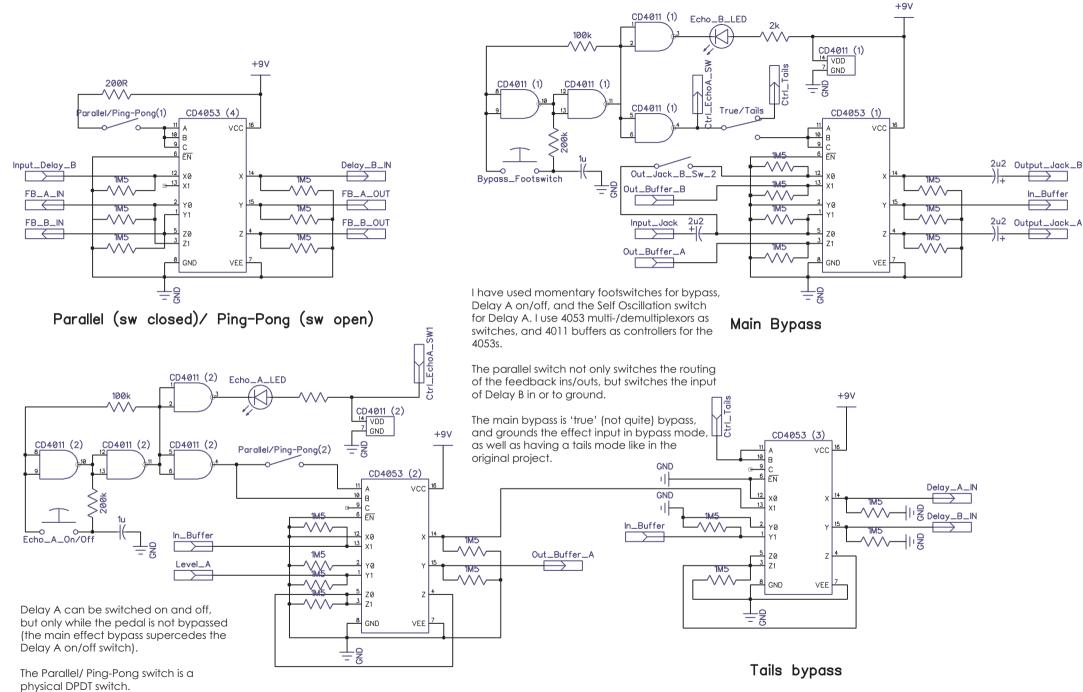
Some connection symbols show "4053 (2.13)", for example. This means a connection to 4053 chip 2, pin 13. See the switching schematic for details.

Some of the 10k resistors show alternate values in brackets. These should be used to increase the level of delays in the alternate resistor values shown on the previous schematic are used.

Input_Jack



Input Buffer



Echo A on/off